MINISTRY OF EDUCATION AND TRAINING HO CHI MINH CITY OPEN UNIVERSITY

COURSE SYLLABUS

I. GENERAL INFORMATION

- 1. Course title in Vietnamese: KIÉN TRÚC MÁY TÍNH
- 2. Course title in English: COMPUTER ARCHITECTURE
- **3.** Knowledge / skills categorization:

□General knowledge

 \boxtimes Basic knowledge

□ Professional knowledge

4. Number of credits

Total	Theory	Practice	Self-study
3	3	0	3(3,0,6)

- 5. In charge of subjects
- a) Faculty / Department / Sub-Department: Information Technology
- b) Lecturer: MSc. Bui Thanh Hieu
 c) Contact email: bt.hieu@ou.edu.vn
 d) Address: Room 604. Faculty of Information

Room 604. Faculty of Information Technology. 35-37 Hồ Hảo Hớn Street, District 1. HCM City

□Specialized knowledge

□Supplementary knowledge

Graduate project / thesis

II. COURSE INFORMATION

1. Course description

The course of Computer Architecture provides students with the basic knowledge about the structure and operation of computers. The course content includes: Introduction to computer architecture, logic circuits, clock and bus, processor, memory, instruction set. Mastering knowledge of computer architecture as the foundation for students to continue to study systems subjects such as Operating System, Computer Networks.

2. Course conditions

#	Course conditions	Course code
1.	Prerequisites	
	None	

2.	Previous courses	
	Programming techniques	ITEC1401
3.	Parallel courses	
	None	

3. Course objectives (COs)

The course will provide students with ability to:

Course objectives (CO)	Description	Related Program Learning Outcomes (PLO)
CO1 (Knowledge)	 Understand the structure and operation of the processor. Understand the operation of the memory. Understand the basic principles of instruction sets. Explain computer structure and operation through processor components, memory, bus, main input/output devices (magnetic disks, monitors, printers). 	PLO3.1
CO2 (Skills)	 Explain the main technical parameters of the computer. Describe the structure, the process of installing computers through a simulation. 	PLO3.2
CO3 (Attitude)	Being aware of the importance of the courseBeing aware of the meaning for each content of the course	PLO3.3

4. Course learning outcomes (CLOs)

After completing this course, students are able to:

Course objectives (CO)	Course learning outcomes (CLO)	Description
	PO1.1	- Explain the structure and operation of the computer
	PO1.2	- Explain the structure and operation of the microprocessor
01	PO1.3	- Explain the operation of the memory
	PO1.4	- Explain the operation of the input/output devices
	PO1.5	- Manipulate the instructions of the microprocessor
	PO2.1	- Explain the main technical parameters of the computer
02	PO2.2	- Manipulate the structure, the process of installing computers through a simulation
03	PO3.1	- Improve self-study, independent problem solving by self

CLOs	PLO3.2	PLO4.5	PLO5.9	PLO13.1	PLO13.2	PLO13.3
PO1.1	2	2	2			
PO1.2	3	3	3			
PO1.3	3	3	3			
PO2.1			3			
PO2.2			4			
PO2.3			4			
PO3.1				4	3	3

Matrix of Course Learning Outcomes (CLOs) and Program Learning Outcomes (PLOs):

1: Not supported

2: Partially supported

3: Supported

4: Highly supported 5: Totally supported

5. Course materials

a) Textbooks

[1] William Stallings, Computer Organization and Architecture: Designing for Performance, Pearson, 2016.

b) Reference materials

[1] David A. Patterson, Computer Architecture: A Quantitative Approach, Elsevier,

2012.

[2] A.S. Tanenbaum, Structured Computer Organization, Pearson, 2013.

Components	Components Assessment		Course learning outcomes (CLO)	Rate (%)
(1)	(2)	(3)	(4)	
A1. Process	A1.1 Classwork and homework		PO1.1, PO1.2, PO1.3, PO2.1, PO2.2, PO3.1	50%
assessment	Total: 01			50%
A2. Final term	A2.1 Essay test		PO1.1, PO1.2, PO1.3, PO2.1, PO2.2, PO3.1	50%
assessment	Total: 01			50%
Total				100%

6. Course assessment

7. Rubrics

a) Midterm assessment rubric (50%)

Students take the midterm test within 60 minutes. This test is not acceptable for using materials and includes 3 chapters:

- Chapter 2: Digital Circuit.
- Chapter 3: Bus System.
- Chapter 5: Memory.

Criteria	CLO	Weigh t	Excellent 10 - 9	Good 8 – 7	Fair 6 - 5	Poor 4 - 0	Point
Chapter 2	CL01.1	50%					5.0
		25%	Use Karnaugh	Miss the ordinary	Do not know about	Do not know about	2.5
Use Karnaugh graph			graph to identify	of variables in	Karnaugh but know	Karnaugh, use the	
to simplify the			the output.	Karnaugh graph.	to use the logic laws	logic law but wrong	
boolean circuit.					to simplify the	in part of it.	
					expressions.		
Draw the logic		25%	Correctly draw the	Miss some gates /	Correctly draw a	Draw incorrectly.	2.5
circuit.			logic circuit.	gates' notation.	part of the circuit.		
Chapter 3	CL01.1	25%					2.5
Data transfer.		25%	Correctly calculate	Only show the	Correctly calculate a	Only one step is	2.5
			the results as step	correct results	few of steps, but the	correct. The rests	
			by step.	without any steps.	result is incorrect.	are incorrect.	
					Maybe due to the		
					unit conversion.		
Chapter 5	CL01.3	25%					2.5

Alternative page		25%	Correctly calculate	Correctly calculate	Only LRU or FIFO	Only a part of LRU	2.5
methods			the alternative	LRU or FIFO or a	is correct.	or FIFO is correct.	
			page methods.	part of the			
				methods.			
	Total	100%					10

b) Final term assessment rubric (50%)

Students take the final test within 60 minutes. This test is not acceptable for using materials and includes 4 chapters:

- Chapter 3: Bus System.
- Chapter 4: CPU.
- Chapter 5: Memory.
- Chapter 6: Data types.

Criteria	CLO	Weigh t	Excellent 10 - 9	Good 8 – 7	Fair 6 - 5	Poor 4 - 0	Point
Chapter 3	CL01.1	20%					2.0
Data tranfers		20%	Correctly calculate the result step by step.	Only show the correct results without any steps.	Correctly calculate a few of steps, but the result is incorrect. Maybe due to the unit conversion.	Only one step is correct. The rests are incorrect.	2.0
Chapter 4	CLO1.2	20%					2.0

CPU		20%	Correctly draw 4 steps of pipelines	Correctly draw 4 steps of pipelines but	Correctly draw 4 steps of pipelines	Correctly draw a part or cannot	2.0
			with 12	more than 12	but less than 12	araw.	
			commands.	commands.	commands.		
Chapter 5	CL01.3	30%					3.0
Memory		10%	Correctly calculate	Correctly calculate	Correctly calculate	The bit qty is	1.0
			the bit qty to	the bit qty to	the bit qty to	almost correct to	
			represent the	represent the virtual	represent the virtual	represent the	
			virtual address and	address or physical	address or physical	virtual address or	
			physical address.	address. The rest one	address.	physical address,	
				is nearly correct.		or cannot	
						calculate.	
		20%	Correctly calculate	Correctly calculate a	Correctly calculate	Correctly	2.0
			the physical	part of the physical	the physical	calculate a few	
			addresses in	addresses in binary	addresses in	physical	
			binary.		decimal.	addresses or	
						cannot calculate.	
Chapter 6	CL01.1	30%					30
Data types		30%	Correctly calculate	Correctly calculate 3	Correctly calculate 2	Correctly	3.0
			these figures: CPI,	in these figures: CPI,	in these figures:	calculate 1 in	
			MIPS, T and	MIPS, T and	CPI, MIPS, T and	these figures:	
			MIPS _A /MIPS _B	MIPS _A /MIPS _B	MIPS _A /MIPS _B	CPI, MIPS, T and	
						MIPS _A /MIPS _B ; or	
						cannot calculate.	
	Total	100%					10
L		1	1	1	1		

8. Teaching plans

Tasalina		forthoom	- (/ 5	alaga time a		a a a a i a m
reaching	DIADS	for ineory	14 7	class-nme	unu per	session
reacting	prano	101 theory	$, \langle \cdots \rangle$	ciubb time	anne por	56551011

Week/session	Content	CLO	Teaching and	Reviews /	Main
			learning	Assessment	documents
			activities		and references
(1)	(2)	(3)	(4)	(5)	(6)
1. Week 1/ Theory Session 1	Chapter1:IntroductiontoComputerArchitecture1.1 Introduction1.1.1 Definition1.1.2 Structure andfunction of computer.1.2 Operation of thecomputer.1.2.1 Classificationofelectronic	PO1.1 PO2.1 PO2.2 PO3.1	Faculty: + Introducing detailed outlines. + Preaching + Ask questions, exercises. + Emphasize the main points. + Specify the requirements for the next class.	A1.1 A2.1	
	computer 1.2.2 General diagram of the computer. 1.2.3 Operation of the computer 1.3 Organization 1.3.1 Physical organization 1.3.2 Booting 1.3.3 Overview of software		Students: + Study in class: listen to lectures, answer questions, solve poses, take notes. + Study at home: watch lectures, draw on the central knowledge, learn related knowledge. +On the LMS system: answer theoretical multiple-choice questions, participate in discussions on the forum.		
2. Week 2/ Theory Session 2	Chapter 1 (cont.) 1.4 Some peripheral devices 1.4.1 Magnetic disk 1.4.2 CD and DVD disk 1.4.3 Monitor 1.4.4 Printer Chapter 2: Circuit Logic 2.1 Introduce 2.2 Boolean algebra and logic gate 2.2.1 Boolean algebra 2.2.2 Logic gate	PO1.1 PO2.1 PO2.2 PO3.1	Faculty: + Preaching + Ask questions, exercises. + Emphasize the main points. + Specify the requirements for the next class. Students: + Study in class: listen to lectures, answer questions, solve poses, take notes. + Study at home: watch lectures, draw on the central knowledge, learn related knowledge.	A1.1 A2.1	Click or tap here to enter text.

			⊥On the IMS		
			system: answer		
			theoretical		
			multiple choice		
			questions		
			questions,		
			discussions on the		
			forum		
3 Week 3/	Chapter 2 (cont.)	DO 1 1	Foculty:	A 1 1	Click or ten here to
J. WEEK J/	Chapter 2 (cont.)	PO 1. 1	Proposing	A1.1 A2.1	onter text
Theory Session 5	2.3 Combination	105.1		A2.1	chief lext.
	circuit		+ Ask questions,		
	2 3 1 Plus circuit		L Emphasiza tha		
	2.3.1 Flus circuit		+ Emphasize the		
	2.5.2 Willus Circuit		Specify the		
	Circuite		+ Specify the		
	2.5 Pagistor		the payt class		
	2.5 Register 2.5.1 Parallel		the next class.		
	registers		Students		
	2 5 2 Shift register		\pm Study in class:		
	2.3.2 Shift Tegister		+ Study III class.		
			answer questions		
			solve poses take		
			notes		
			+ Study at home		
			watch lectures		
			draw on the central		
			knowledge learn		
			related knowledge		
			$\pm On$ the LMS		
			system: answer		
			theoretical		
			multiple-choice		
			questions.		
			participate in		
			discussions on the		
			forum.		
4. Week 4/	Chapter 3: Clock and	PO1.1	Faculty:	A1.1	
Theory Session 4	bus	PO3.1	+ Preaching	A2.1	
	3.1 Introduction.	1 0011	+ Ask questions.		
	3.2 Clock and bus		exercises.		
	3.2.1 Overview of		+ Emphasize the		
	clock		main points.		
	3.2.2 Speed		+ Specify the		
	3.2.3 Overview of		requirements for		
	bus		the next class.		
	3.2.4 Architecture				
	of bus		Students:		
			+ Study in class:		
			listen to lectures,		
			answer questions,		
			solve poses, take		
			notes.		
			+ Study at home:		
			watch lectures,		
			draw on the central		
			knowledge, learn		
			related knowledge.		
			+On the LMS		
			system: answer		
			theoretical		
			multiple-choice		

			questions, participate in discussions on the forum		
5. Week 5 / Theory Session 5	Chapter 3 (cont.) 3.3 Structure of bus 3.3.1 Structure of Core i 3.3.2 Norm I/O (IDE, SATA, USB). 3.3.3 Bus PCI & PCI Express. 3.3.4 I/O Method 3.4 Introduction to analog and digital signal	PO1.1 PO3.1	Faculty: + Preaching + Ask questions, exercises. + Emphasize the main points. + Specify the requirements for the next class. Students: + Study in class: listen to lectures, answer questions, solve poses, take notes. + Study at home: watch lectures, draw on the central knowledge, learn related knowledge. +On the LMS system: answer theoretical multiple-choice questions, participate in discussions on the forum	A1.1 A2.1	
6. Week 6/ Theory Session 6	Chapter 4: Processors 4.1 Structure 4.1.1 General structure 4.1.2 Registers 4.1.3 An arithmetic and logic unit. 4.1.4 Control unit 4.2 Enhancement the performance of processors 4.2.1 Clock impact 4.2.2 Pipelines 4.2.3 Executing instruction parallel. 4.2.4 Using cache memory.	PO1.2 PO1.5 PO3.1	Faculty: + Preaching + Ask questions, exercises. + Emphasize the main points. + Specify the requirements for the next class. Students: + Study in class: listen to lectures, answer questions, solve poses, take notes. + Study at home: watch lectures, draw on the central knowledge, learn related knowledge. +On the LMS system: answer theoretical multiple-choice questions, participate in discussions on the forum.	A1.1 A2.1	

7 Weels 7/	Chamter 4 (comt)	DO1 2	Ecoultry	A 1 1	
7. week //	Chapter 4 (cont.)	PO1.2	Faculty:	A1.1	
Theory Session /	4.3 Multithreading	PO1.5	+ Preaching	A2.1	
	and Chip	PO1.5	+ Ask questions,		
	Multiprocessors	P05.1	Exercises.		
	Chapter 5: Mamory		+ Emphasize the		
	5.1 Memory		\perp Specify the		
	Hierarchy		requirements for		
	merareny.		the next class		
			the next clubb.		
			Students:		
			+ Study in class:		
			listen to lectures,		
			answer questions,		
			solve poses, take		
			notes.		
			+ Study at home:		
			watch lectures,		
			draw on the central		
			knowledge, learn		
			related knowledge.		
			+On the LMS		
			theoretical		
			multiple-choice		
			questions.		
			participate in		
			discussions on the		
			forum.		
8. Week 8/	Chapter 5 (cont.)	PO1.3	Faculty:	A1.1	
Theory Session 8	5.2 Internal memory		+ Preaching	A2.1	
	5.2.1 Bit memory		+ Ask questions,		
	5.2.2 Organization		exercises.		
	5.2.3 Categories		+ Emphasize the		
			main points.		
			+ Specify the		
			the next class		
			the next class.		
			Students:		
			+ Study in class:		
			listen to lectures,		
			answer questions,		
			solve poses, take		
			notes.		
			+ Study at home:		
			watch lectures,		
			knowledge loarn		
			related knowledge		
			+On the LMS		
			system: answer		
			theoretical		
			multiple-choice		
			questions,		
			participate in		
			discussions on the		
0 11 01		DO1.2	forum.	A 1 1	
9. Week 9/	Chapter 5 (cont.)	PO1.3	Faculty:	A1.1	
Theory Session 9	5.3 1 Definition	PO3 1	+ Ask questions	A2.1	
	5.3.2 Paging	1 0 0 . 1	exercises.		

	5.2.2 41				
	5.3.3 Alternative		+ Emphasize the		
	page methods		main points.		
			+ Specify the		
	Chapter 6 (3 lessons):		requirements for		
	Instruction sets		the next class.		
	6.1 Introduction				
	6.2 Characteristics		Students:		
			+ Study in class:		
			listen to lectures.		
			answer questions		
			solvo posos, tako		
			solve poses, take		
			notes.		
			+ Study at nome:		
			watch lectures,		
			draw on the central		
			knowledge, learn		
			related knowledge.		
			+On the LMS		
			system: answer		
			theoretical		
			multiple-choice		
			questions.		
			participate in		
			discussions on the		
			forum		
10 Week 10/	Chapter 6 (cont.)	DO1 4	Focultu:	A 1 1	
Theory Session	Chapter 0 (cont.)	PO1.4	Propohing	A1.1	
Theory Session	6.5 Data types	P05.1	+ Preaching	A2.1	
10	(integer, noat,).		+ Ask questions,		
	6.4 Addressing		exercises.		
	methods.		+ Emphasize the		
	Immediate.		main points.		
	Direct.		+ Specify the		
	Indirect		requirements for		
	Register		the next class.		
	Register indirect				
	Displacement		Students:		
	Stack		+ Study in class:		
			listen to lectures.		
			answer questions.		
			solve poses take		
			notes		
			+ Study at home		
			watch lectures		
			draw on the control		
			knowledge learn		
			related knowledge		
			icialeu kilowieuge.		
			+On the LIMS		
			system: answer		
			theoretical		
			multiple-choice		
			questions,		
			participate in		
			discussions on the		
			forum.		

9. Course regulations

- Regulations on the submission of assignments and tests: Students who do not submit their homework and report assignments on time on the LMS, are deemed not to submit

their works.

- Attendance regulations: students are requested to attend all classes
- Exam regulations banning:
- Class rules: students are asked to keep order and participate in answering questions and doing classwork.

DEAN OF FACULTY (Sign and specify full name) Dr. Le Xuan Truong EDITOR

(Sign and specify full name) MSc. Bui Thanh Hieu